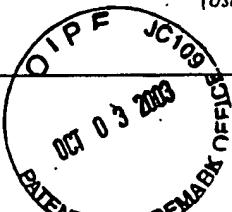


Form PTO-1449 <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> <i>(Use several sheets if necessary)</i>		Docket Number 306812002500	Application Number 10/057,232
		Applicant	David M. LEWIS, et al.
		Filing Date January 25, 2002	Group Art Unit 2825
		Mailing Date September 30, 2003	



**U.S. PATENT DOCUMENTS**

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO

**OTHER DOCUMENTS**

*(including author, title, Date, Pertinent Pages, Etc.)*

Examiner Initials	Ref. No.	Title
<i>JHM</i>	1.	Kluwer Academic Publishers, Vaughn Betz, Jonathan Rose, Alexander Marquardt, <u>Architecture and CAD for Deep-Submicron FPGAs</u> , Chapter 2.1 (pp. 11-18), Chapter 4 (pp. 63-103), Chapter 5 (pp. 105-126), and Chapter 7 (pp. 151-190), (March 1999)

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